X-1436 US

Title Of Invention:

DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN PATENT APPLICATION USING AN **APPLICATION DATA SHEET (37 CFR 1.76)**

Docket No.

INTEGRATED CIRCUIT AND METHOD OF PRODUCING A CARRIER WAFER FOR AN INTEGRATED CIRCUIT
As a below named inventor(s), I/We declare that:
This declaration is directed to:
The attached application, or
Application No filed on (if applicable);
I/We believe I/We am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;
I/We have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;
I/We acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including material information which became available while the application was pending in the United States Patent and Trademark Office; and
All statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.
FULL NAME OF INVENTOR(S)
Inventor One: Soon-Shin Chee Date 1/26/04
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